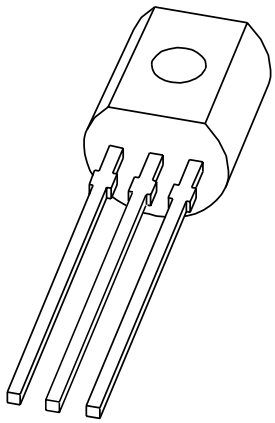
DATA SHEET



2N4126
PNP general purpose transistor

Product specification
Supersedes data of September 1994
File under Discrete Semiconductors, SC04

1997 Mar 25

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PNP general purpose transistor 2N4126

FEATURES

- Low current (max. 200 mA)
- Low voltage (max. 25 V).

APPLICATIONS

- General purpose switching and amplification, e.g. small-signal audio-frequency applications.

DESCRIPTION

PNP transistor in a TO-92; SOT54 plastic package.
NPN complement: 2N4124.

PINNING

PIN	DESCRIPTION
1	collector
2	base
3	emitter

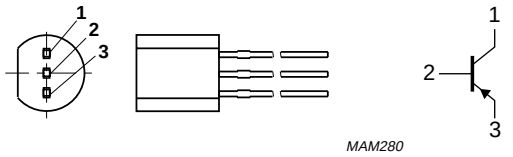


Fig.1 Simplified outline (TO-92; SOT54) and symbol.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–25	V
V_{CEO}	collector-emitter voltage	open base	–	–25	V
I_{CM}	peak collector current		–	–300	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	–	500	mW
h_{FE}	DC current gain	$I_C = -2$ mA; $V_{CE} = -1$ V	120	360	
f_T	transition frequency	$I_C = -10$ mA; $V_{CE} = -20$ V; f = 100 MHz	250	–	MHz

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–25	V
V_{CEO}	collector-emitter voltage	open base	–	–25	V
V_{EBO}	emitter-base voltage	open collector	–	–4	V
I_C	collector current (DC)		–	–200	mA
I_{CM}	peak collector current		–	–300	mA
I_{BM}	peak base current		–	–100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	500	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	250	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

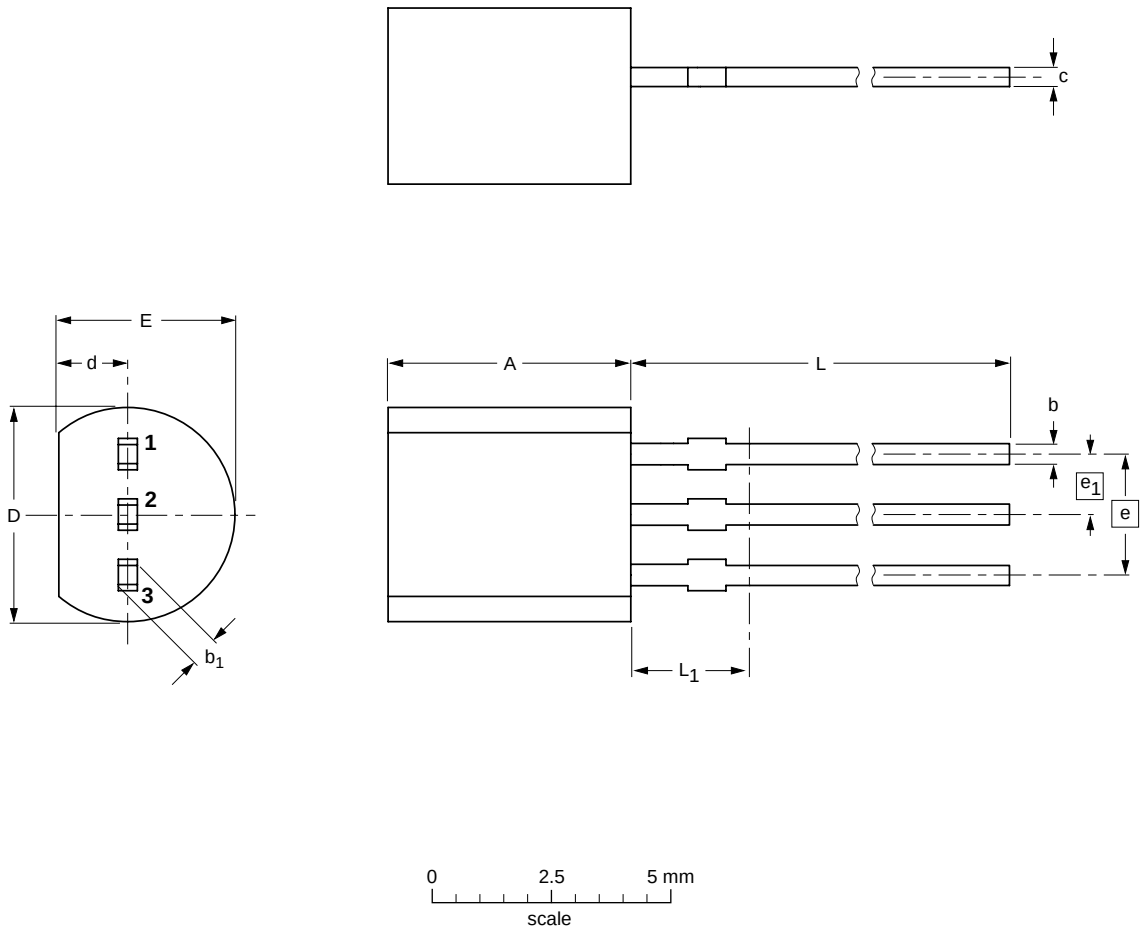
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0$; $V_{CB} = –20$ V	–	–50	nA
I_{EBO}	emitter cut-off current	$I_C = 0$; $V_{EB} = –3$ V	–	–50	nA
h_{FE}	DC current gain	$I_C = –2$ mA; $V_{CE} = –1$ V; note 1	120	360	
		$I_C = –50$ mA; $V_{CE} = –1$ V; note 1	60	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = –50$ mA; $I_B = –5$ mA; note 1	–	–400	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = –50$ mA; $I_B = –5$ mA; note 1	–	–950	mV
C_c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = –5$ V; f = 1 MHz	–	4.5	pF
C_e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = –0.5$ V; f = 1 MHz	–	10	pF
f_T	transition frequency	$I_C = –10$ mA; $V_{CE} = –20$ V; f = 100 MHz	250	–	MHz
F	noise figure	$I_C = –100$ μA; $V_{CE} = –5$ V; $R_S = 1$ kΩ; f = 10 Hz to 15.7 kHz	–	4	dB

Note

1. Pulse test: $t_p \leq 300$ μs; $\delta \leq 0.02$.

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads **SOT54**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b_1	c	D	d	E	e	e_1	L	L_1(1)
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT54		TO-92	SC-43			97-02-28

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

NOTES

NOTES

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