



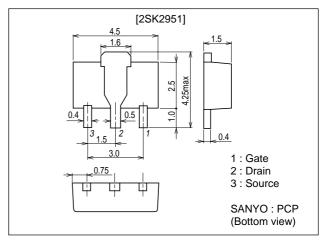
# **Ultrahigh-Speed Switching Applications**

### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.

## **Package Dimensions**

unit : mm 2062A



# **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		200	٧
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		1	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	4	Α
Allowable Power Dissipation	D-	Mounted on a ceramic board (250mm <sup>2</sup> X0.8mm)	1.5	W
	PD	Tc=25°C	3.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	200			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =200V, V <sub>GS</sub> =0			100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±15V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	2.0		3.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =0.5A	0.4	0.8		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	ID=0.5A, VGS=10V		2.5	3.5	Ω

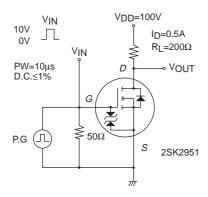
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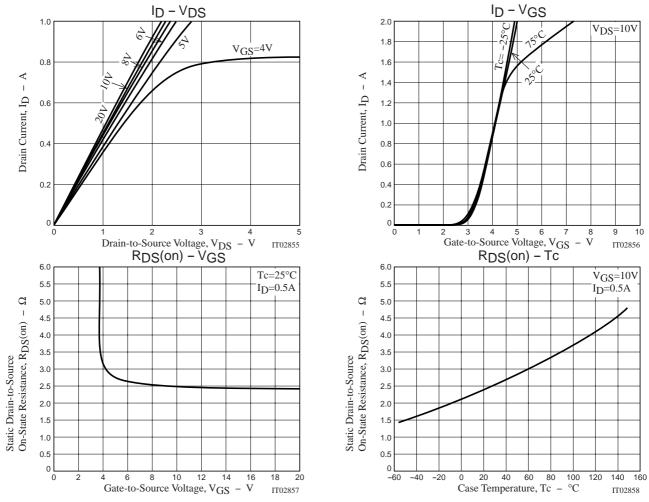
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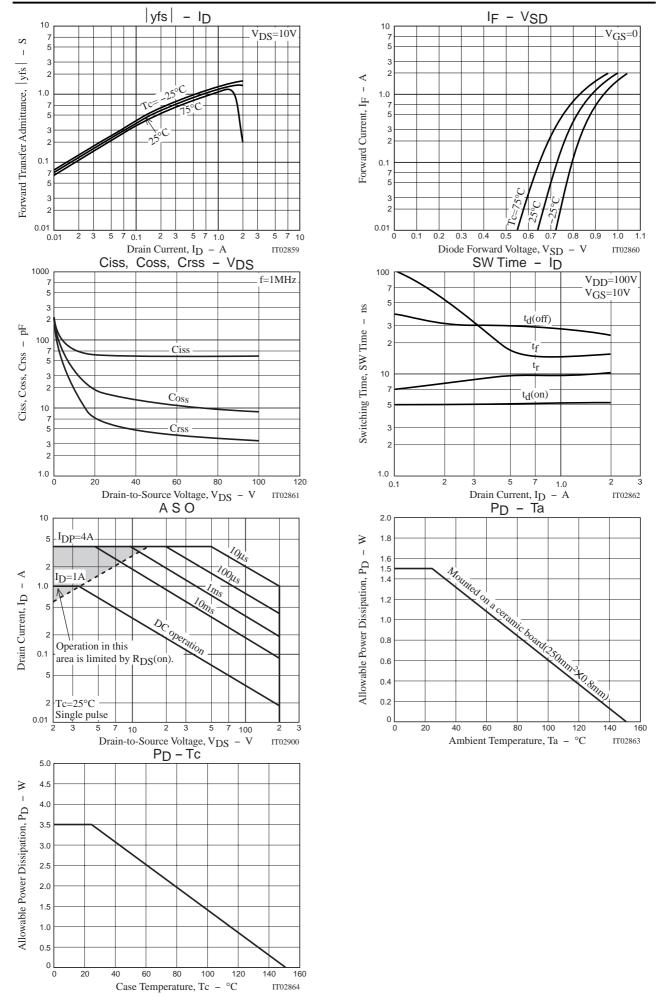
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Parameter	Symbol	Conditions	Ratings			- Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		60		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		18		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		7		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		5		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		10		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		30		ns
Fall Time	tf	See specified Test Circuit		18		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>G</sub> S=0		1.0	1.5	V

## **Switching Time Test Circuit**







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